

FIG. 1

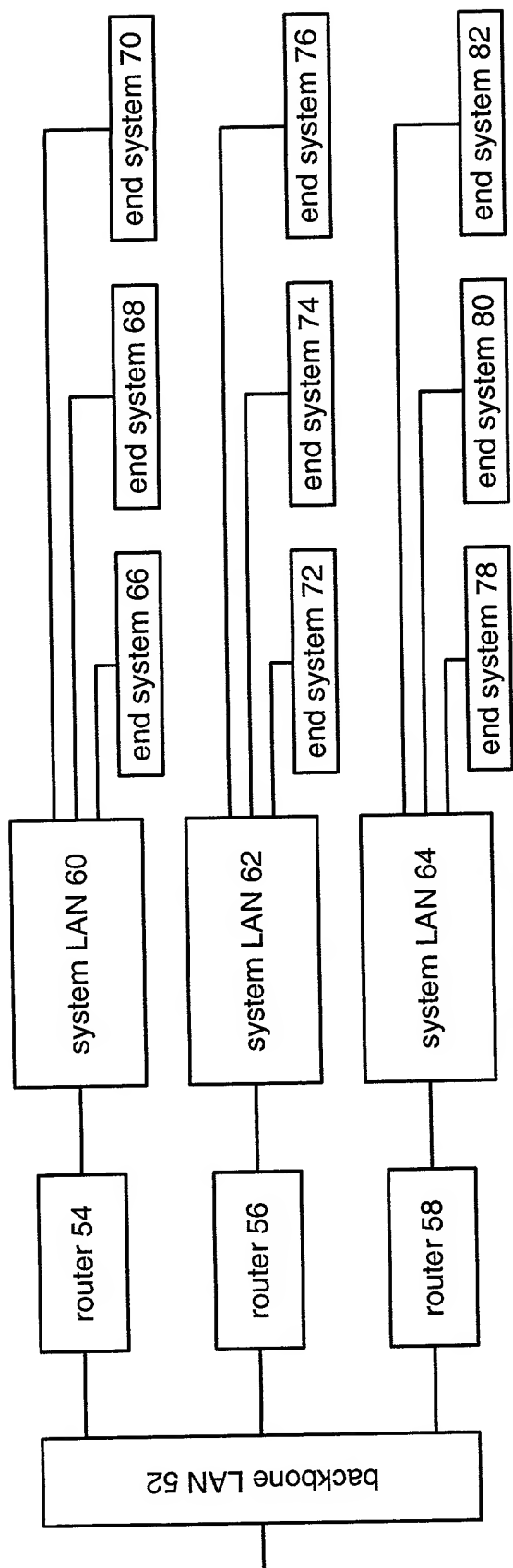


FIG. 2

LAN 18 - 30

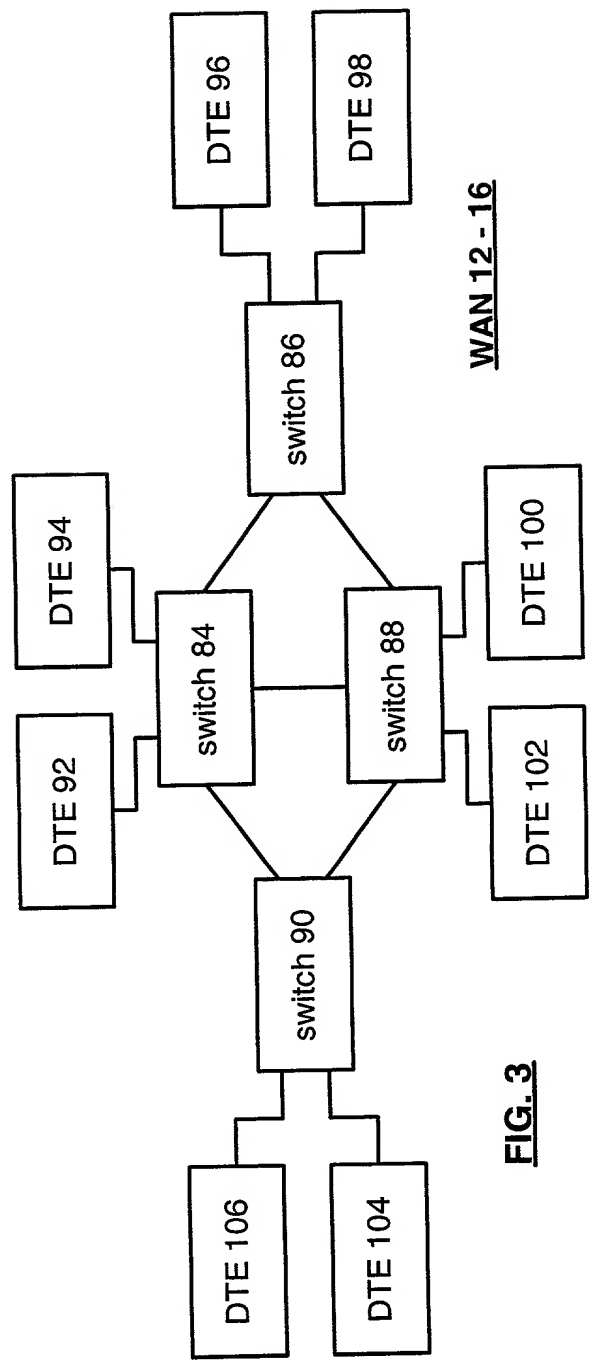


FIG. 3

WAN 12 - 16

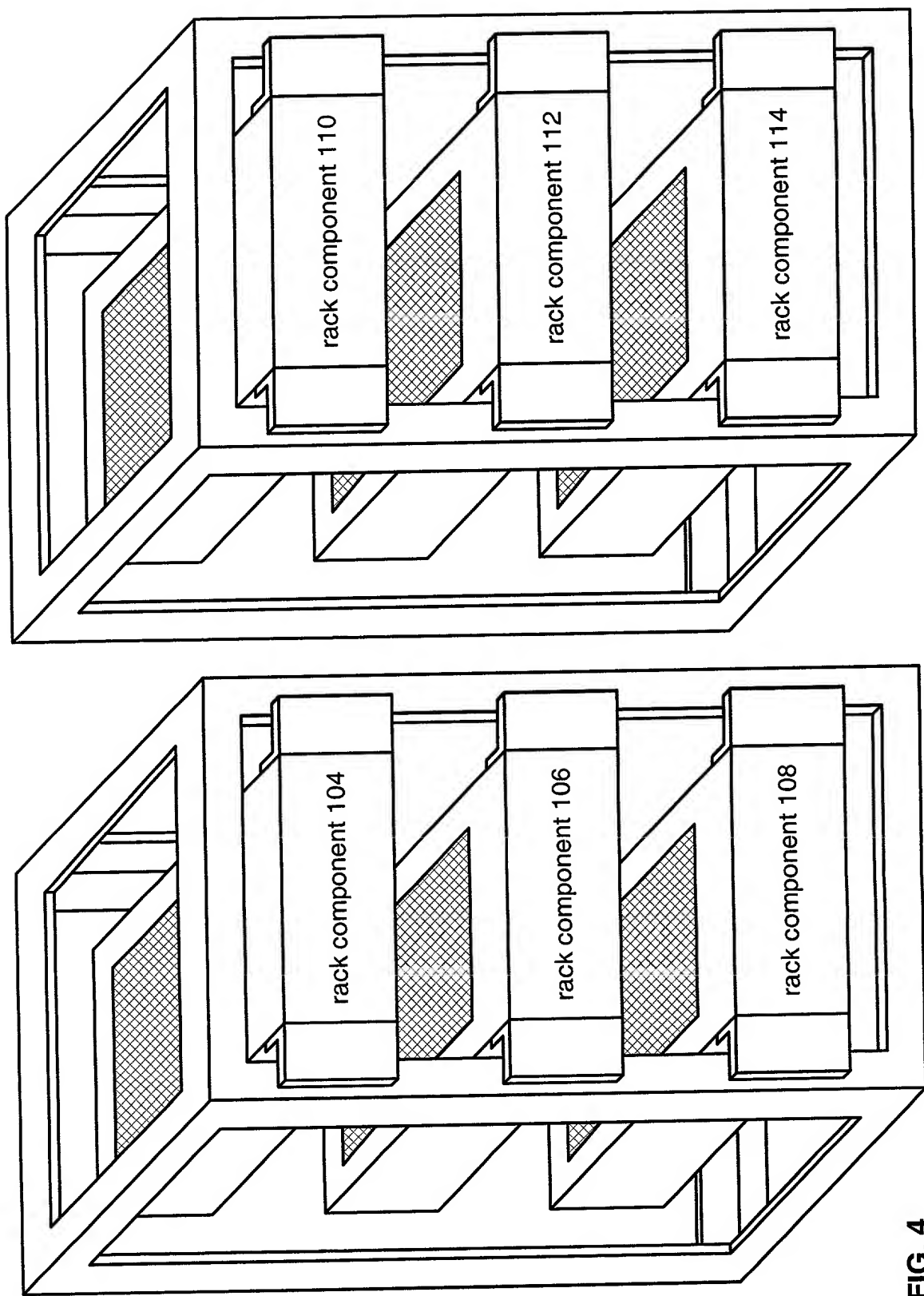


FIG. 4

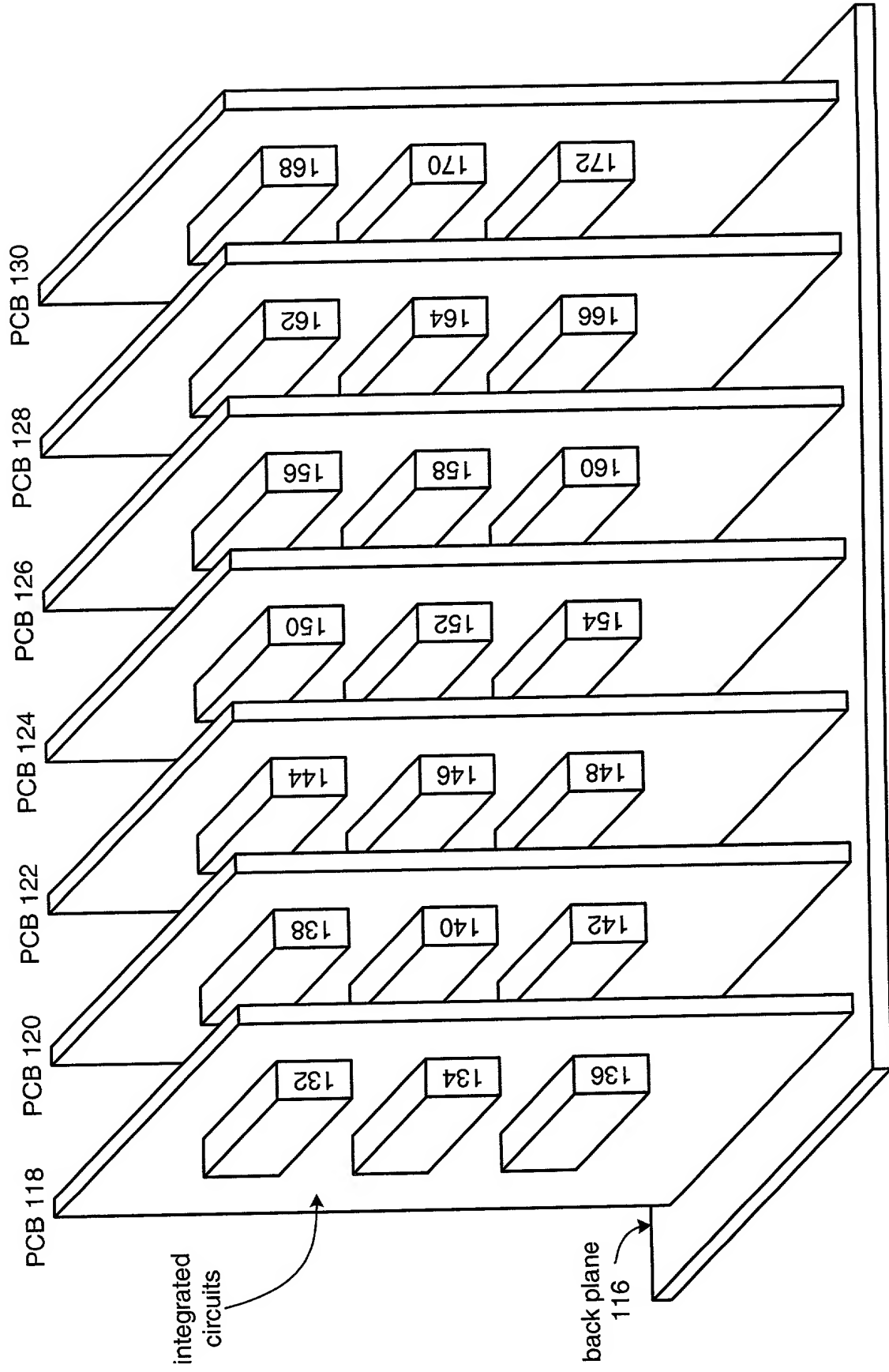


FIG. 5

rack component 104 - 114

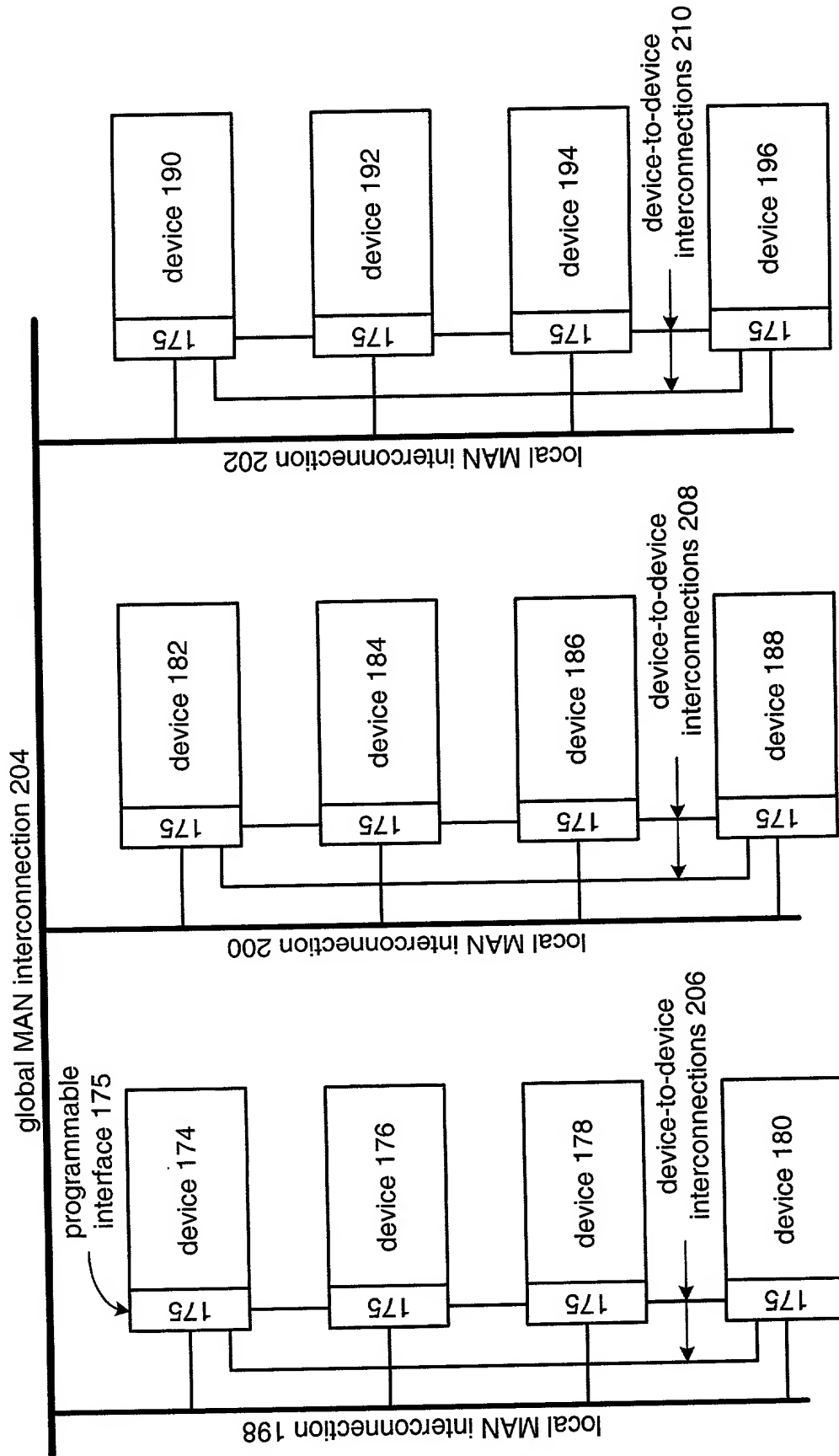


FIG. 6

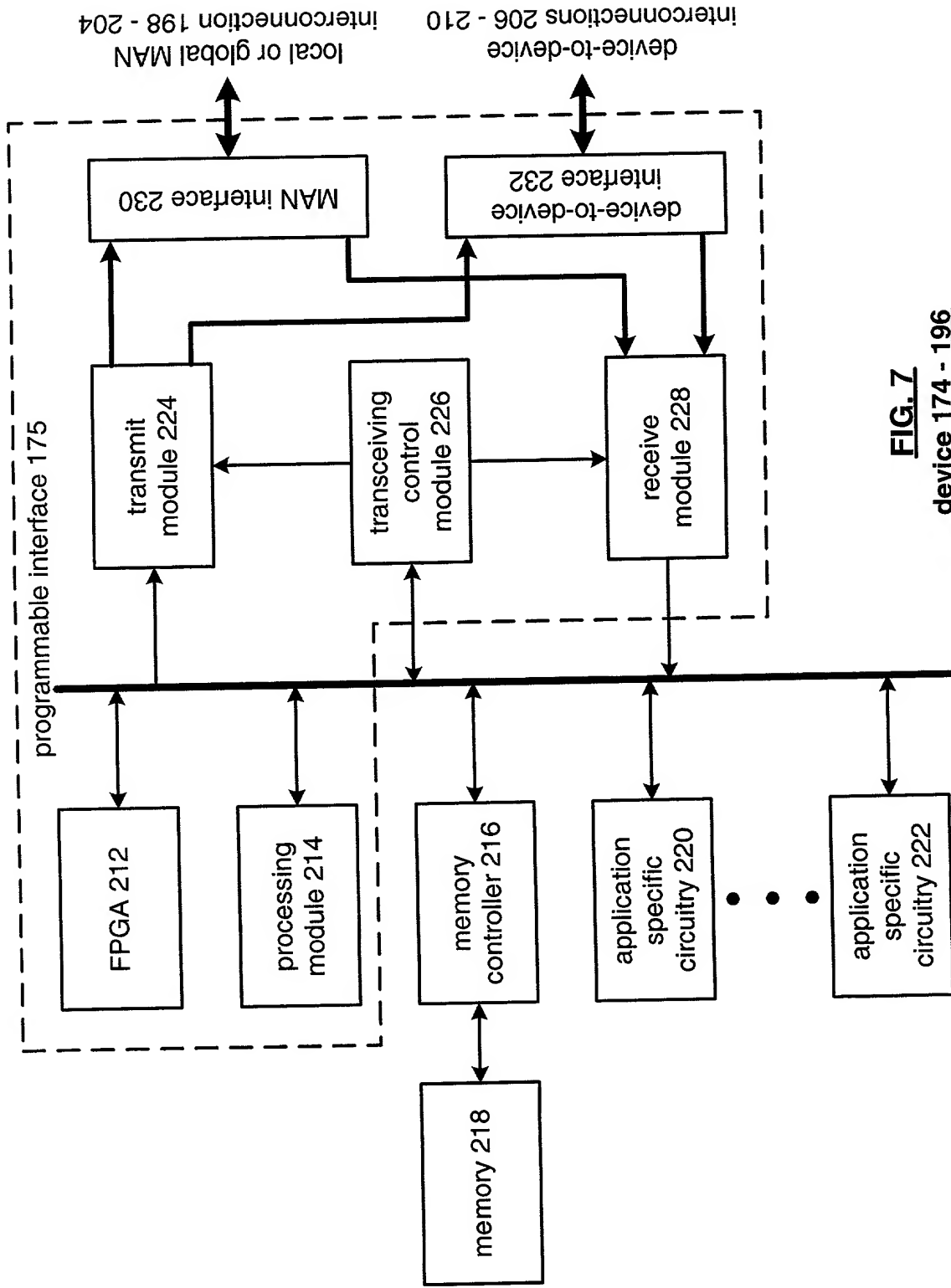


FIG. 7
device 174 - 196

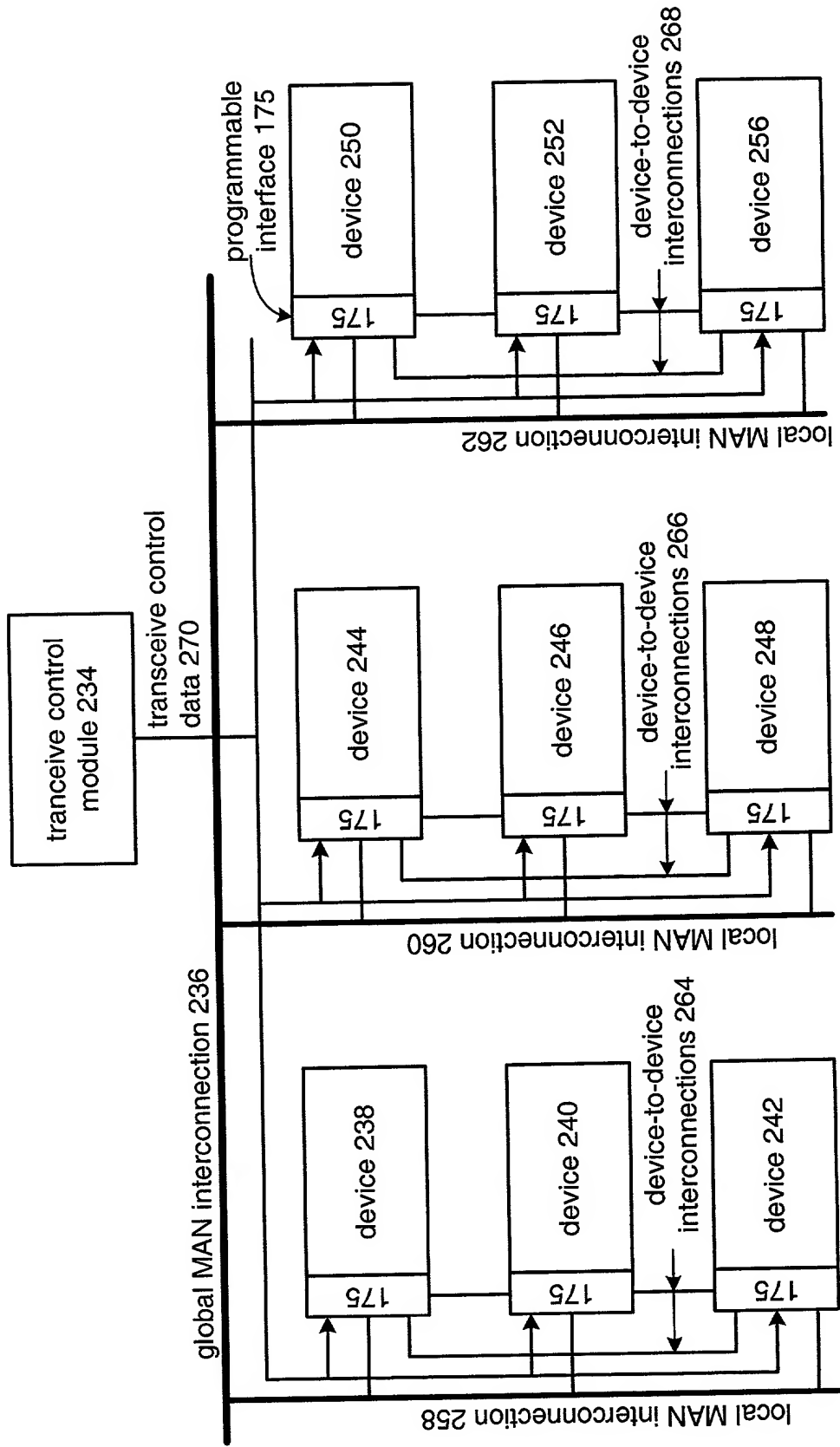


FIG. 8

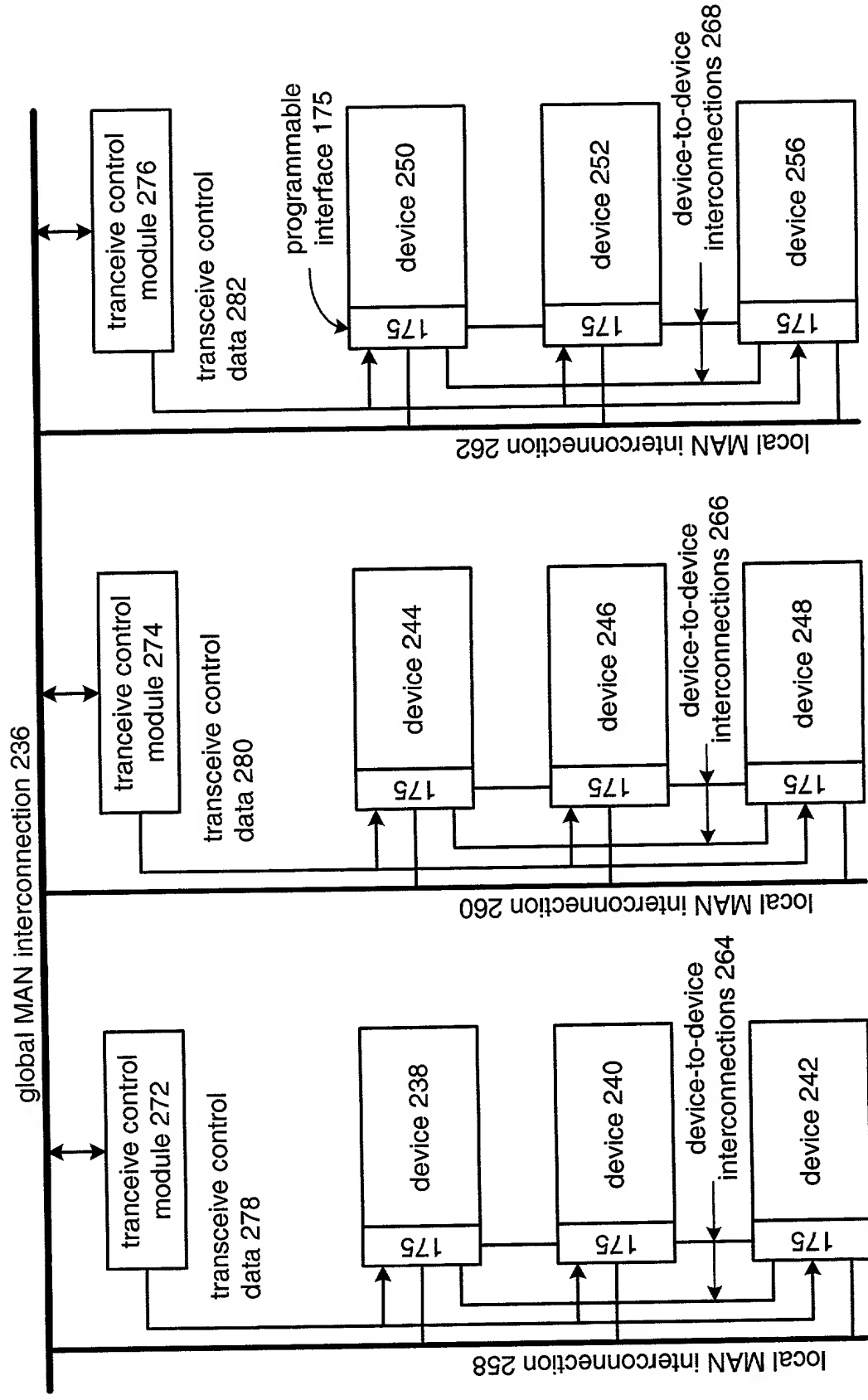
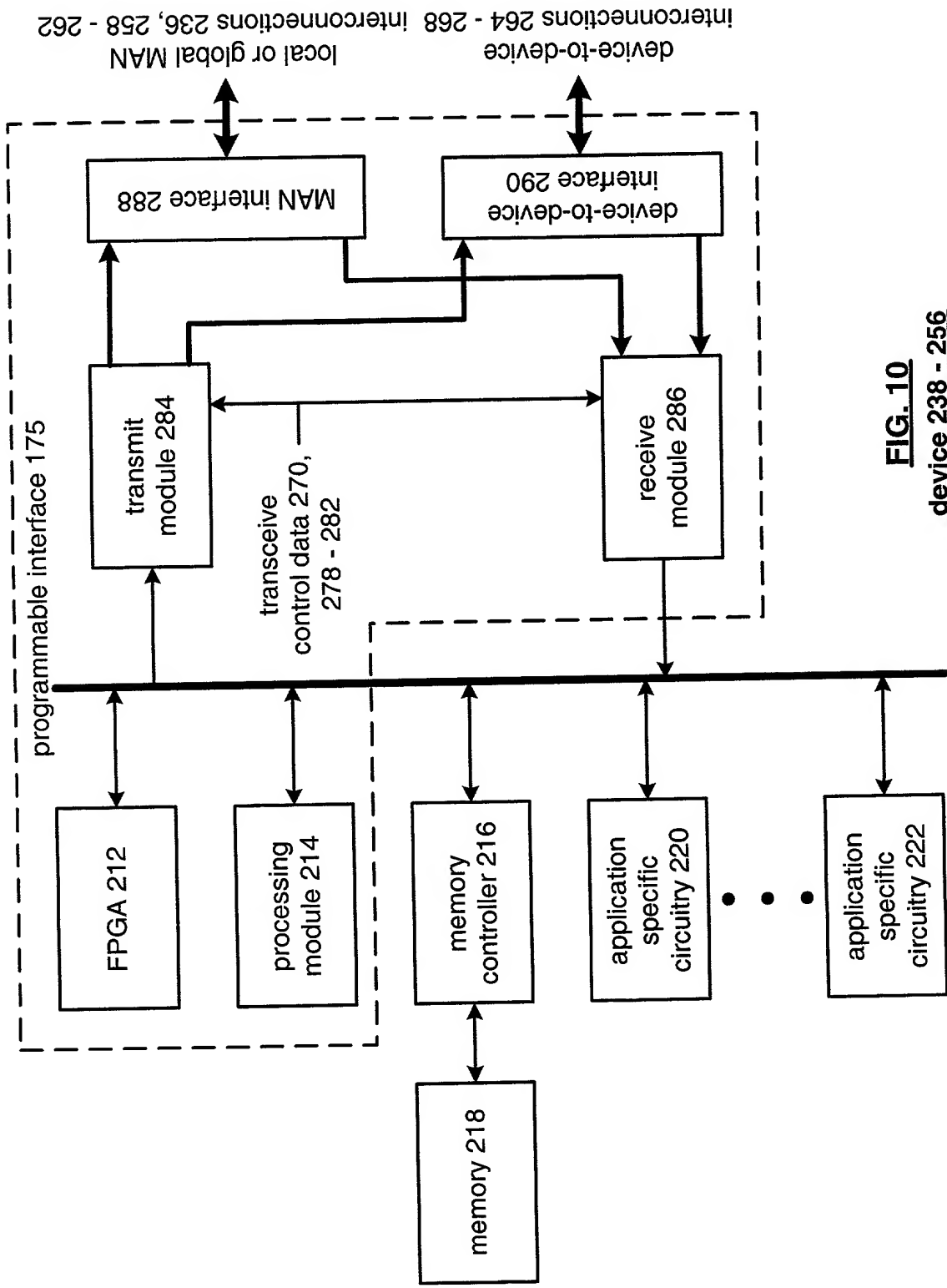


FIG. 9



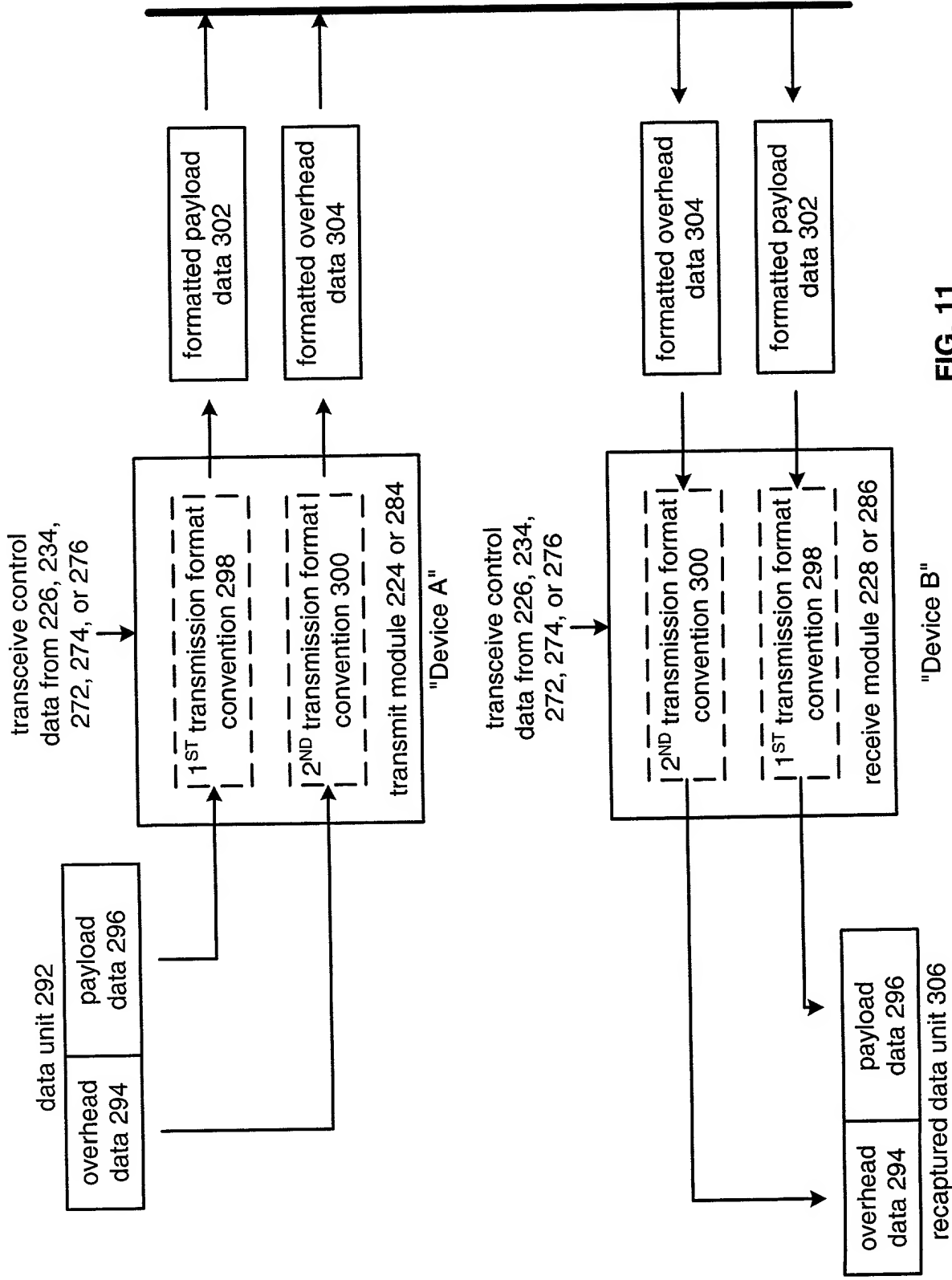


FIG. 11

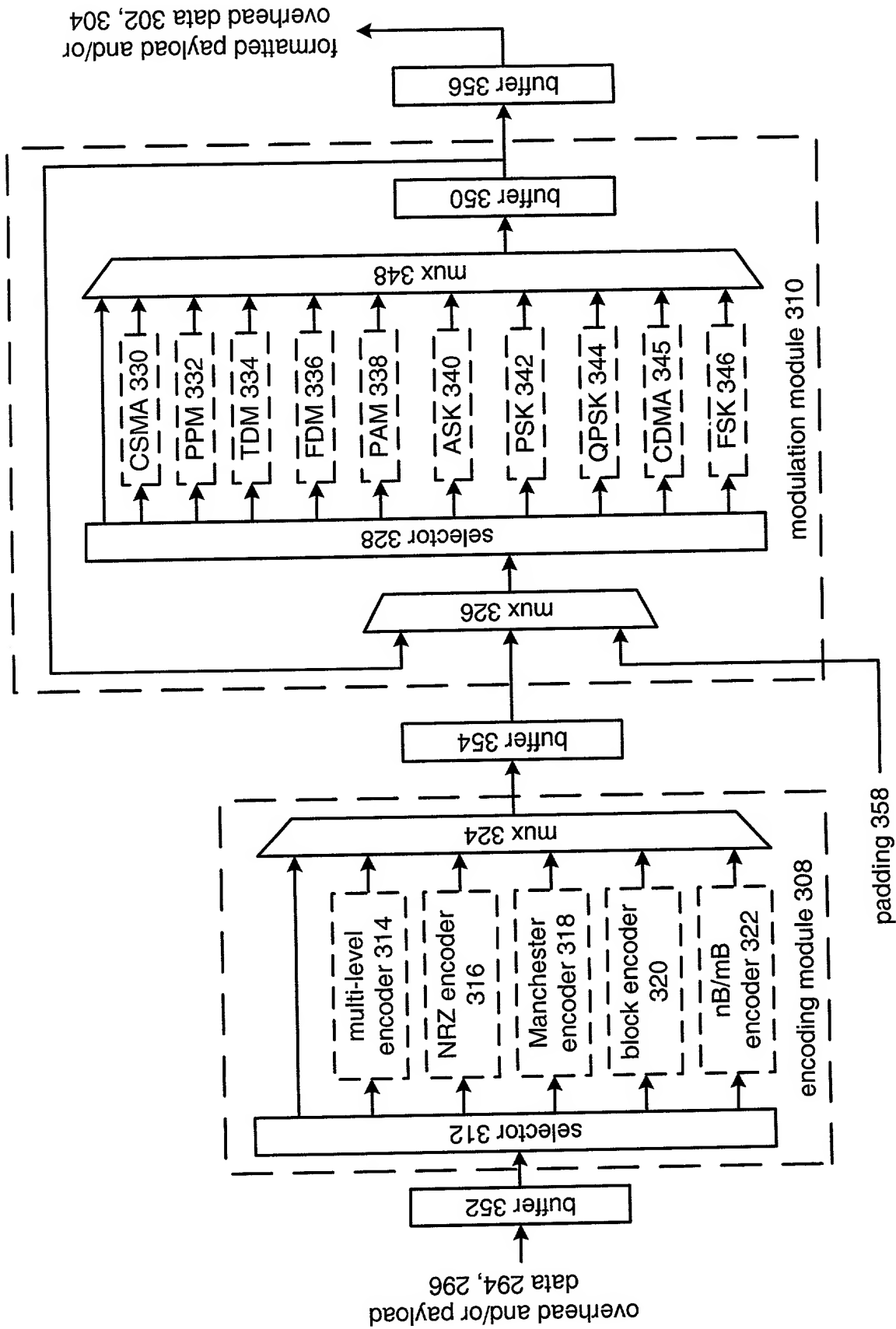


FIG. 12

transmit module 224 or 284

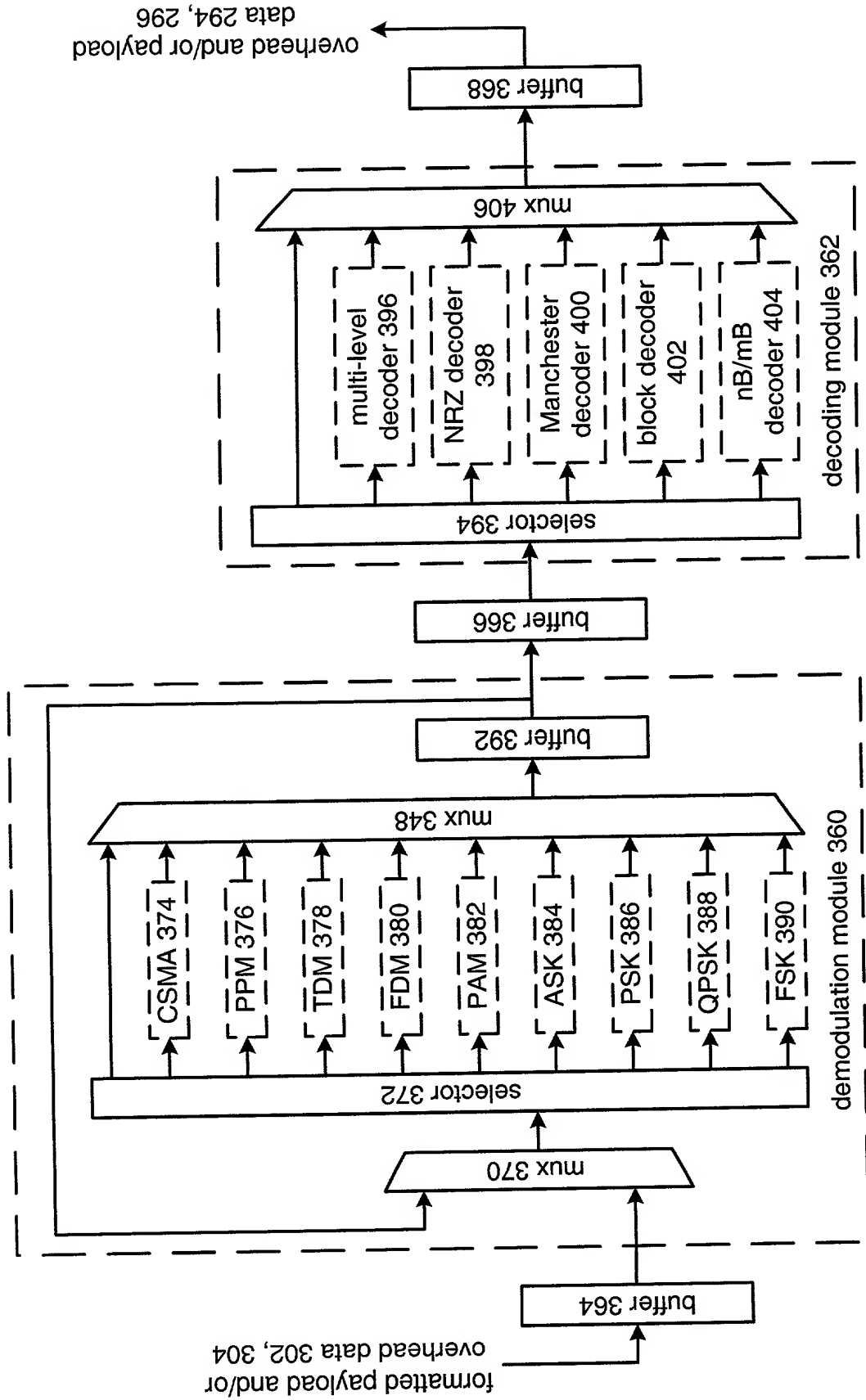


FIG. 13

receive module 228 or 286

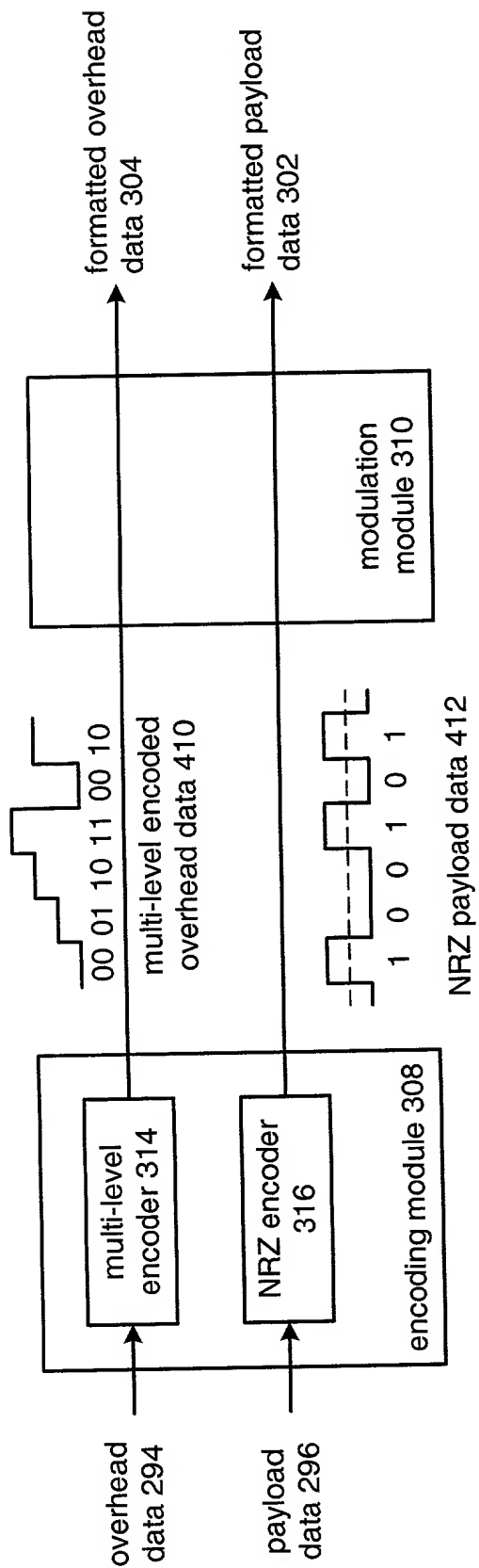


FIG. 14

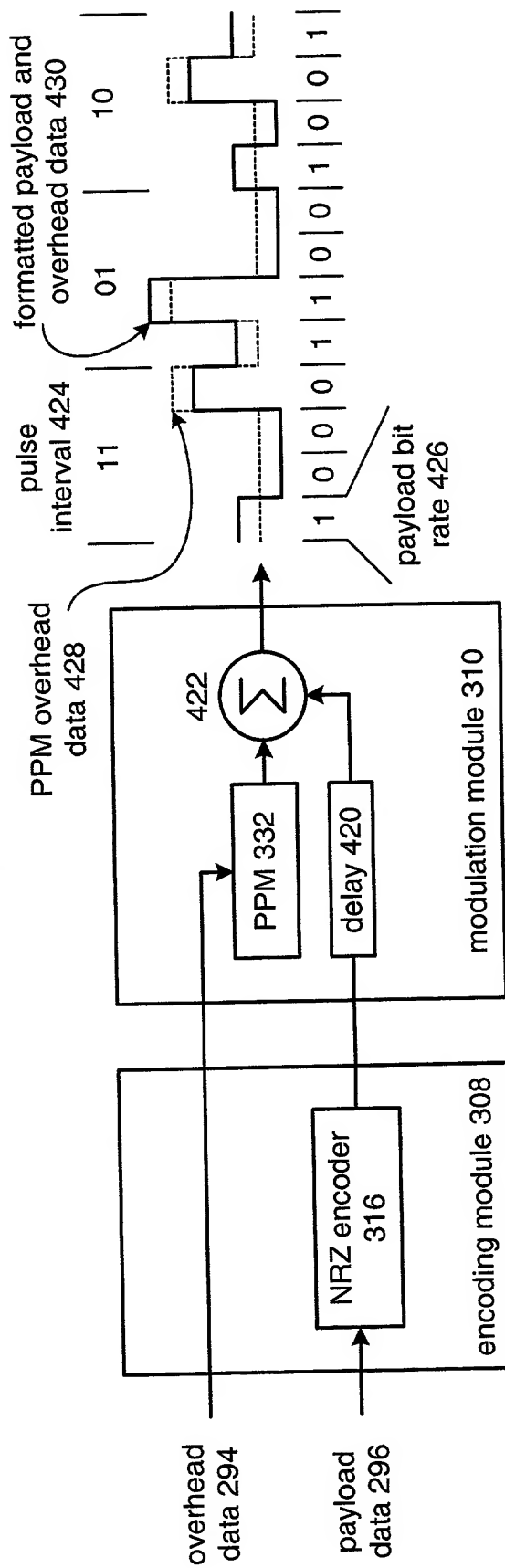


FIG. 15

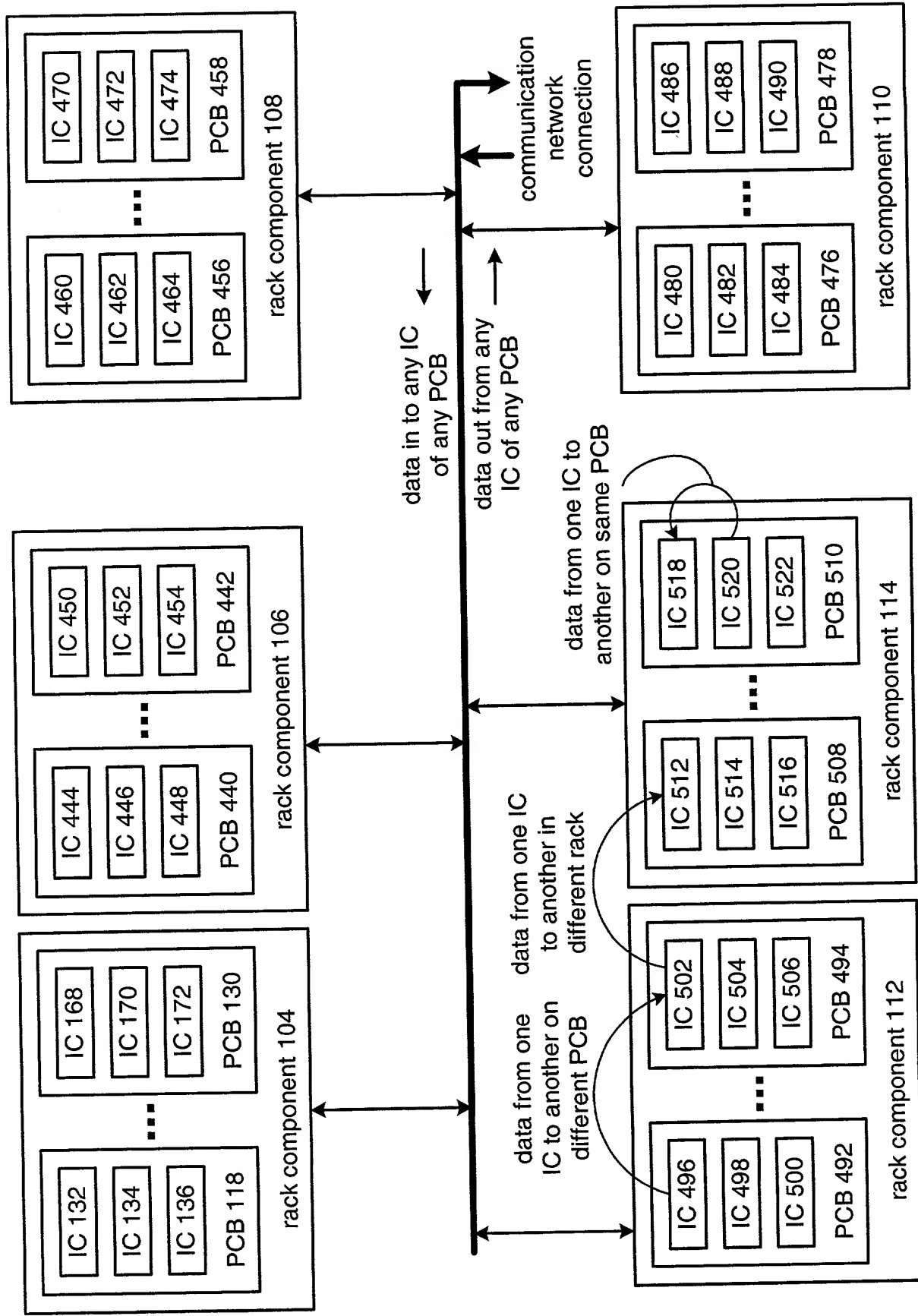


FIG. 16

transmission assignments 524		
source	destination	scheme
132-118-104	134-118-104	A
132-118-104	136-118-104	B
132-118-104	x-130-104	C
132-118-104	x-x-106	D
132-118-104	x-x-108	D
132-118-104	x-x-110	E
132-118-104	x-x-112	E
132-118-104	x-x-114	E
⋮	⋮	⋮

FIG. 17

scheme-resource assignment 526		
scheme	resources	encode/mod/S-P
A	1 path	1-0-S
A	2 paths	1-0-P
A	1-OH, 1-PL	2-1-S
⋮	⋮	⋮
B	1 path	2-0-S

FIG. 18

encoding-modulation-serial/parallel table 528				
encode/mod/S-P	OH encoding	payload encoding	OH modulation	payload mod
1-0-S	multi-level	NRZ	none	none
1-0-P	multi-level	NRZ	none	none
2-1-S	multi-level	nB/mB	none	none
0-1-S	none	NRZ	PPM	mix
⋮	⋮	⋮	⋮	⋮
				S-Tx of OH and PL P-Tx of OH and PL OH:path1; PL:path2 S-Tx of OH and PL

FIG. 19

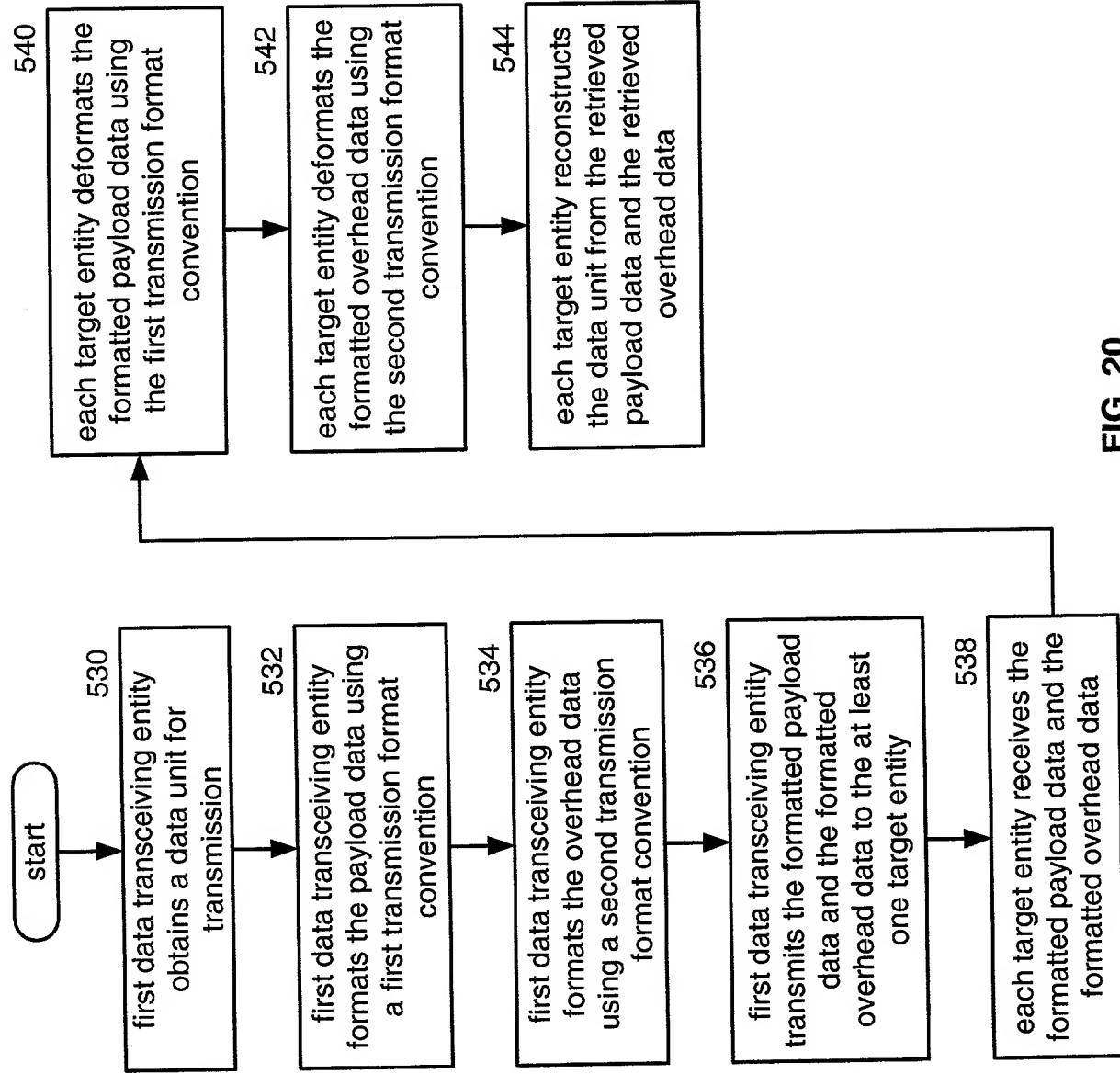


FIG. 20

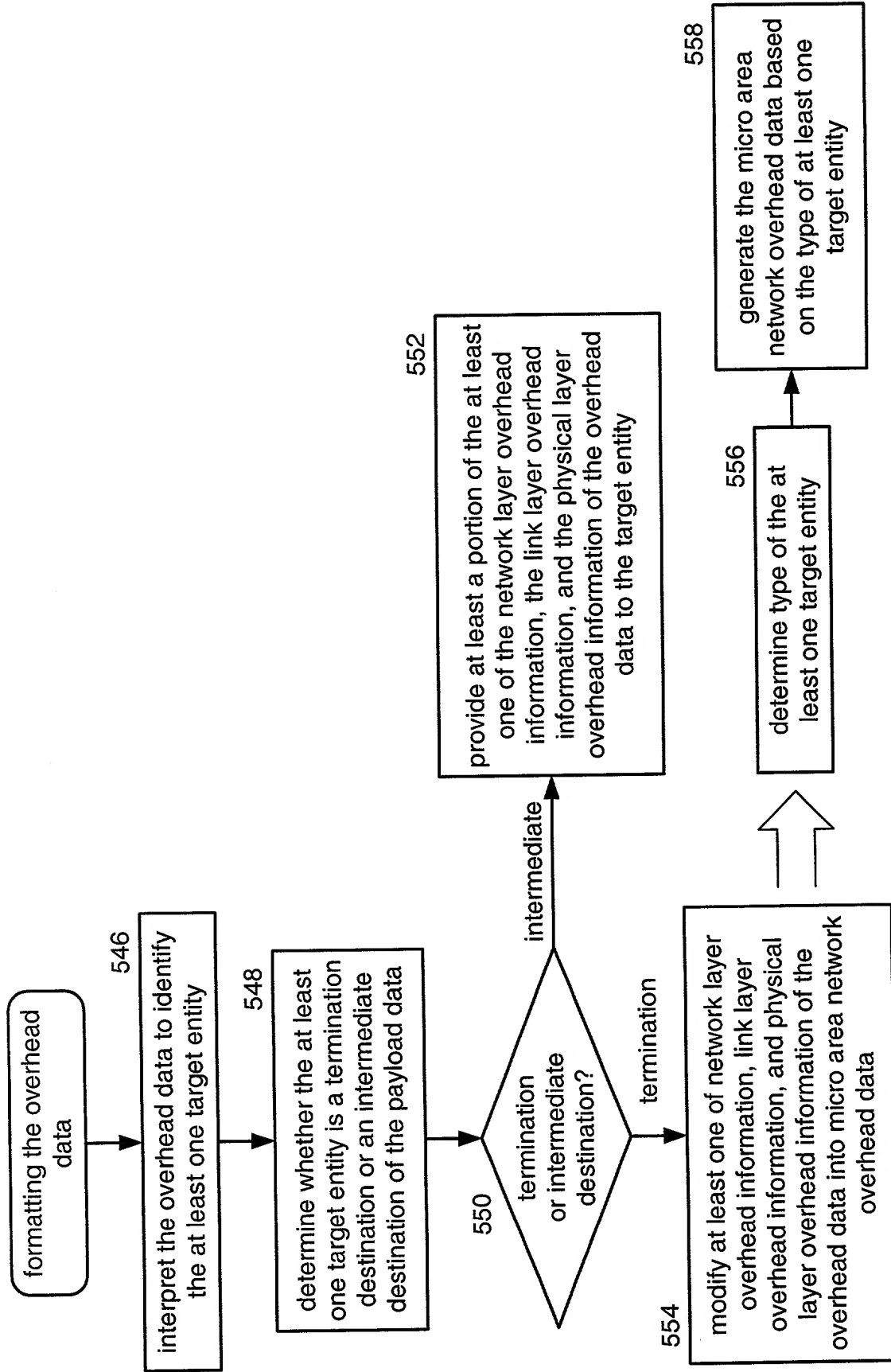


FIG. 21

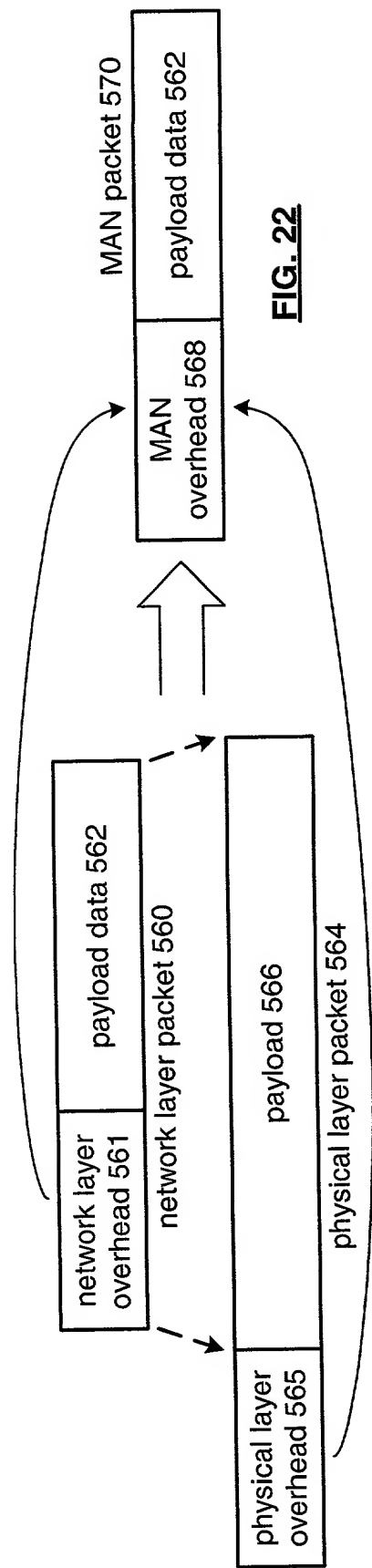


FIG. 22

1st packet xmitted
within MAN as is

obtained series of physical layer packets 575

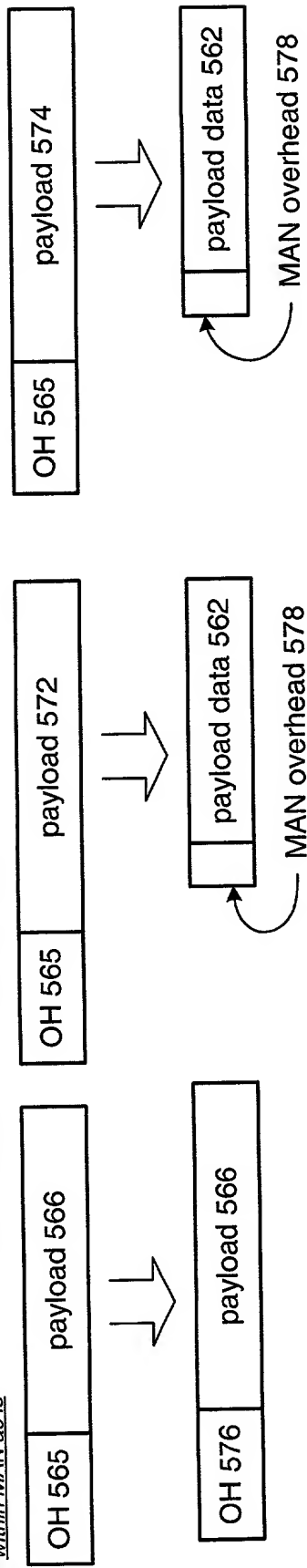


FIG. 23

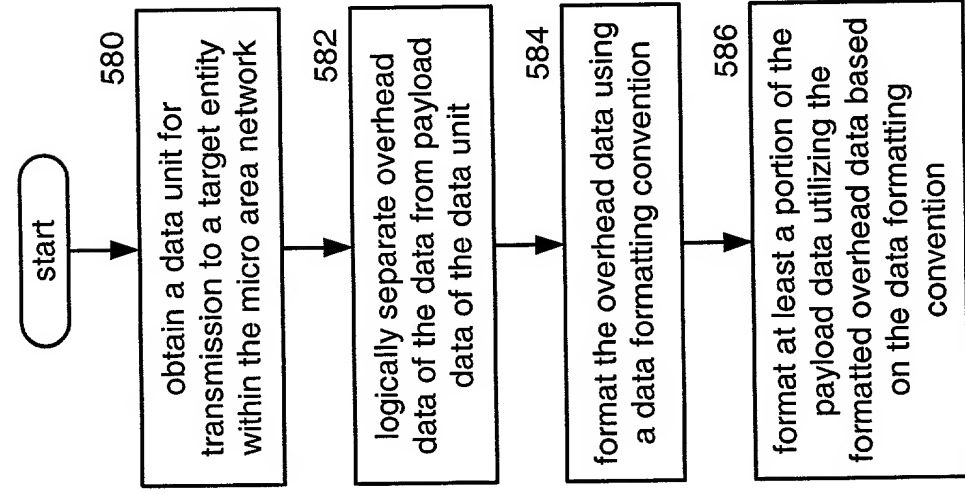


FIG. 24

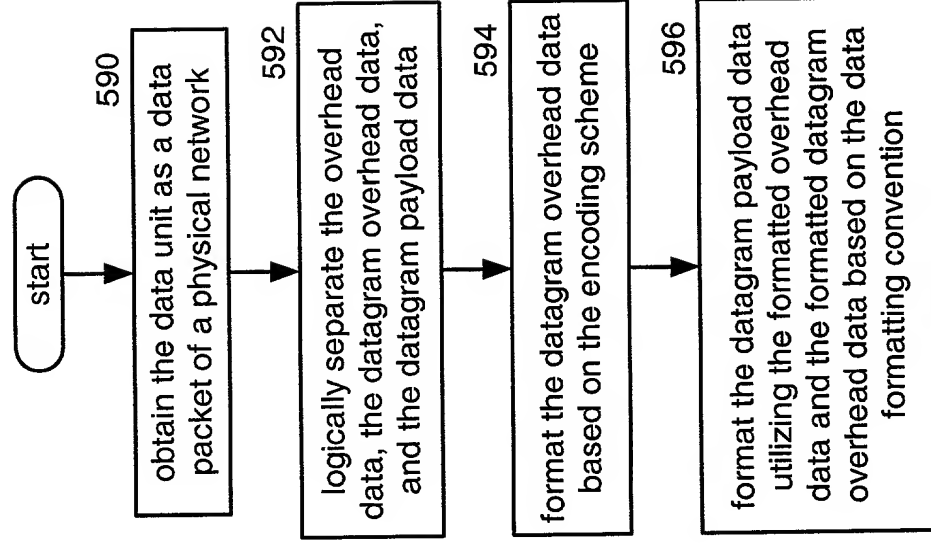


FIG. 25

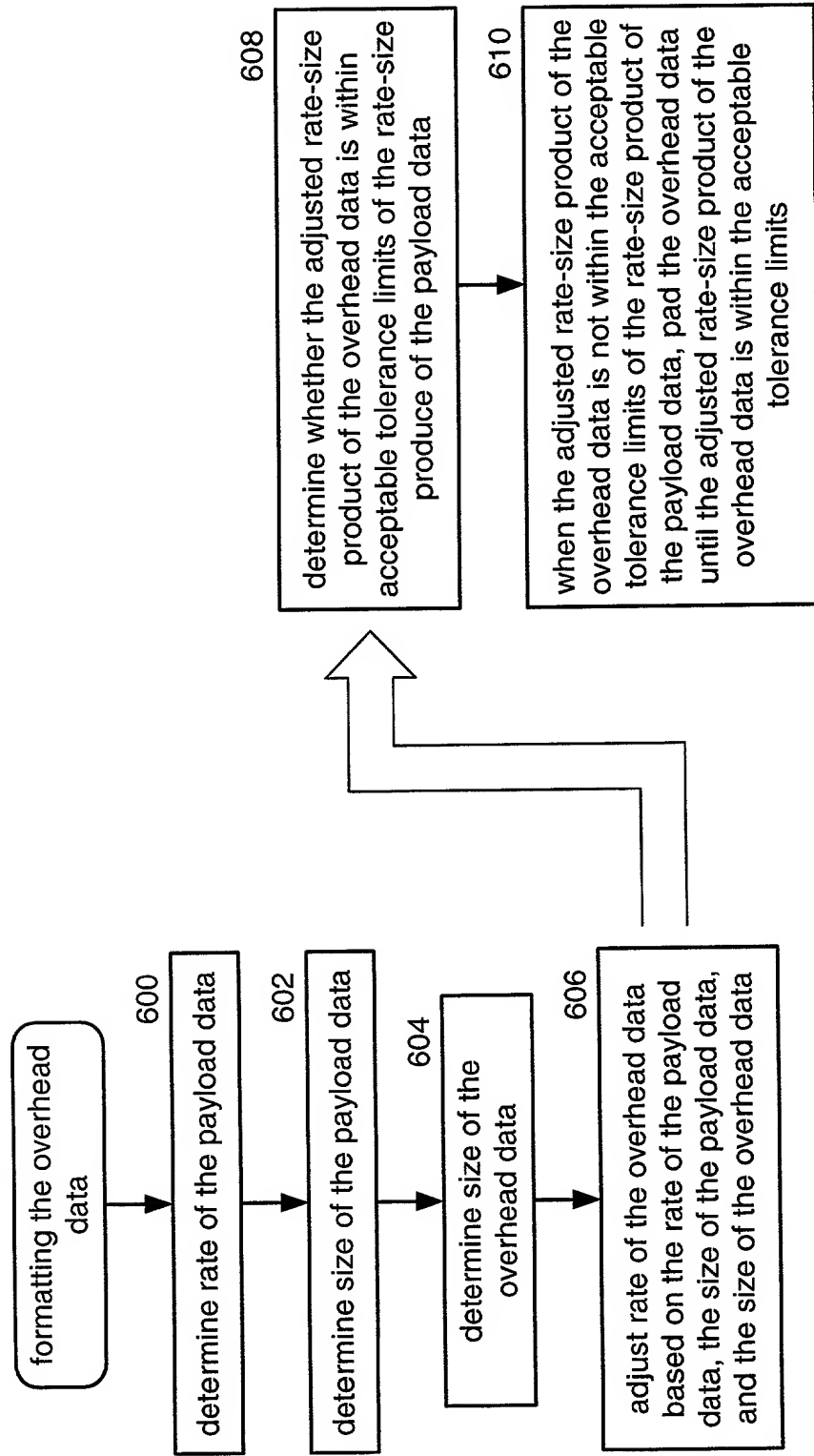
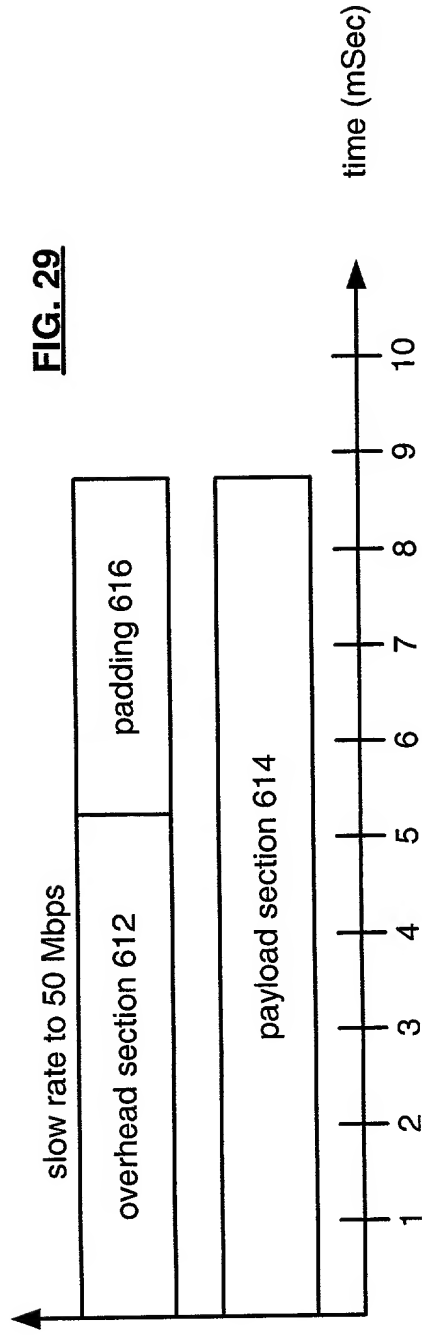
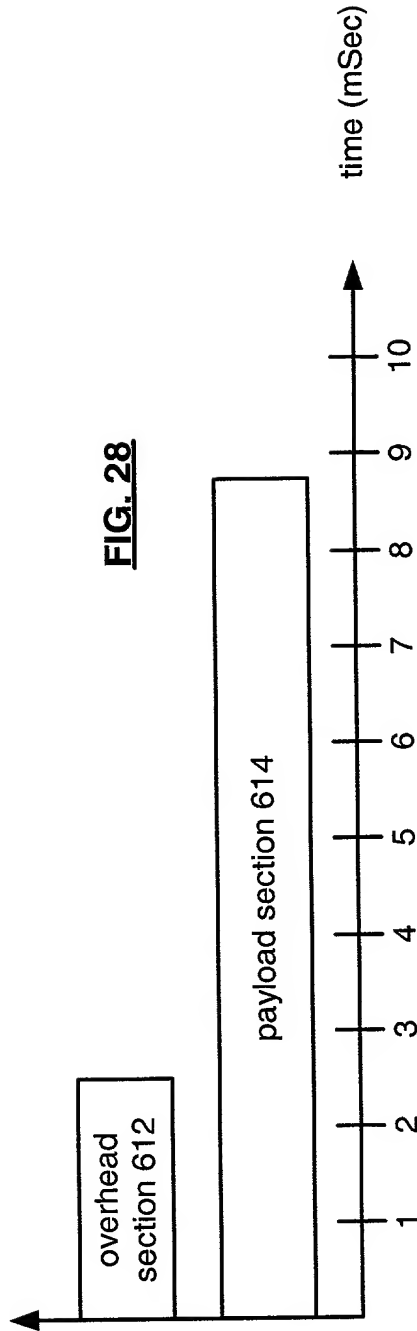
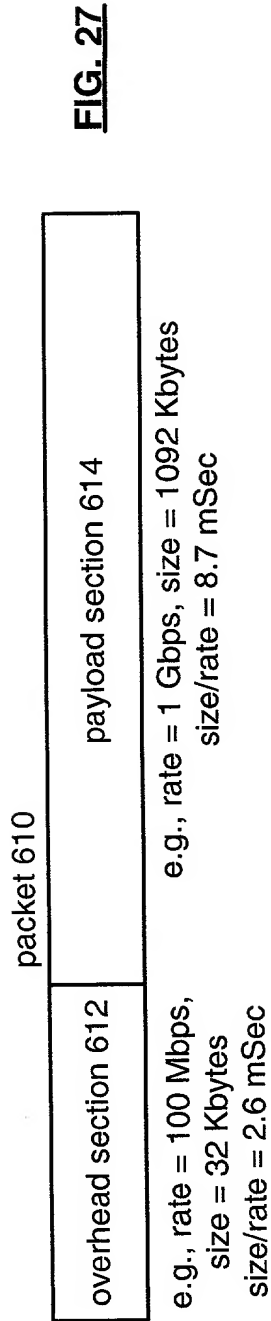


FIG. 26



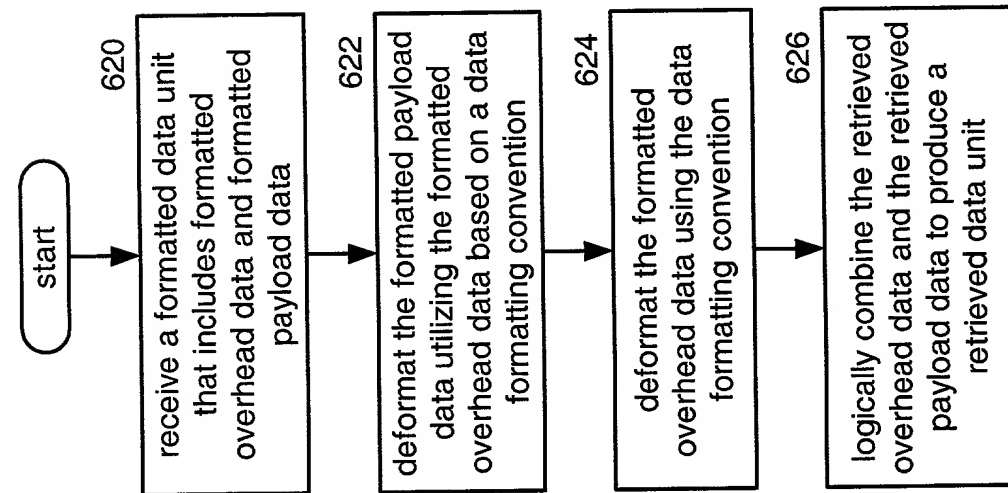


FIG. 30

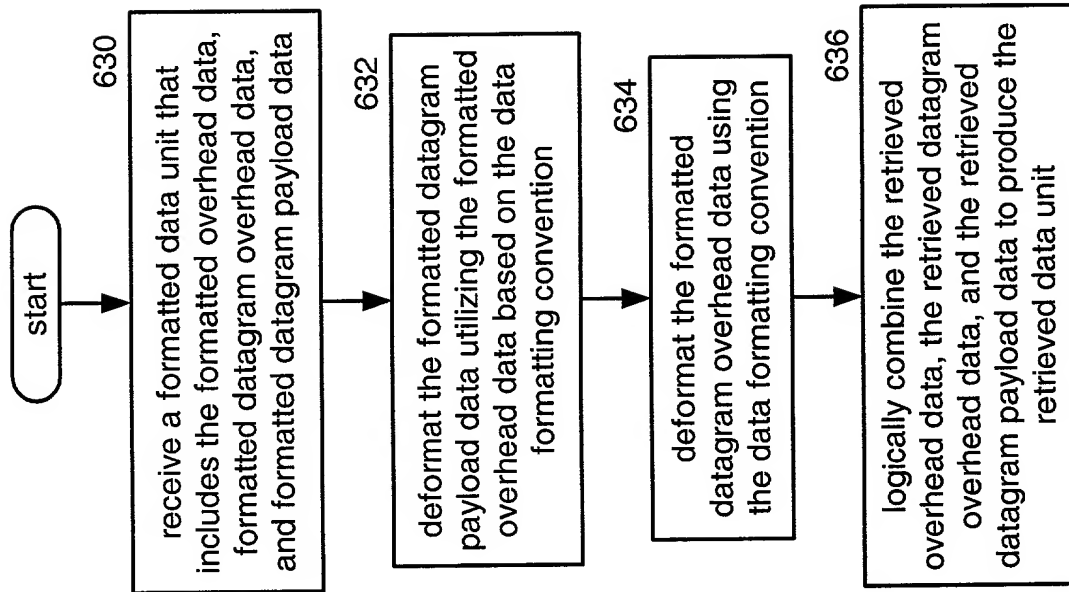


FIG. 31

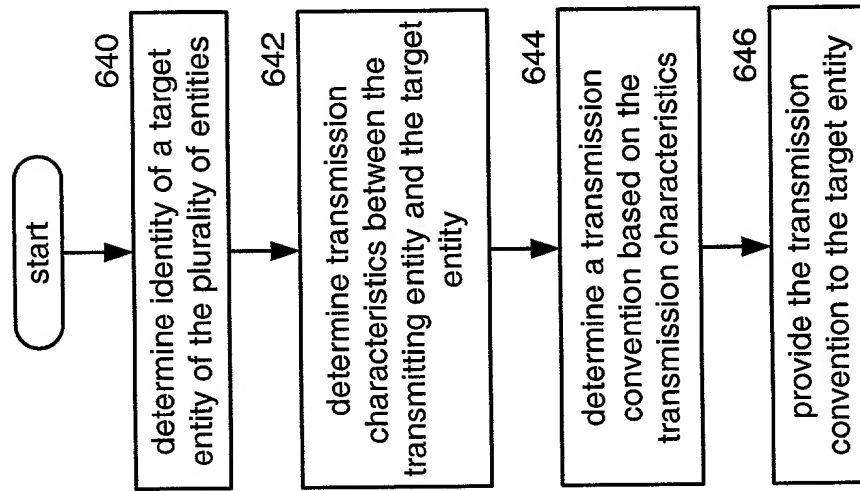


FIG. 32

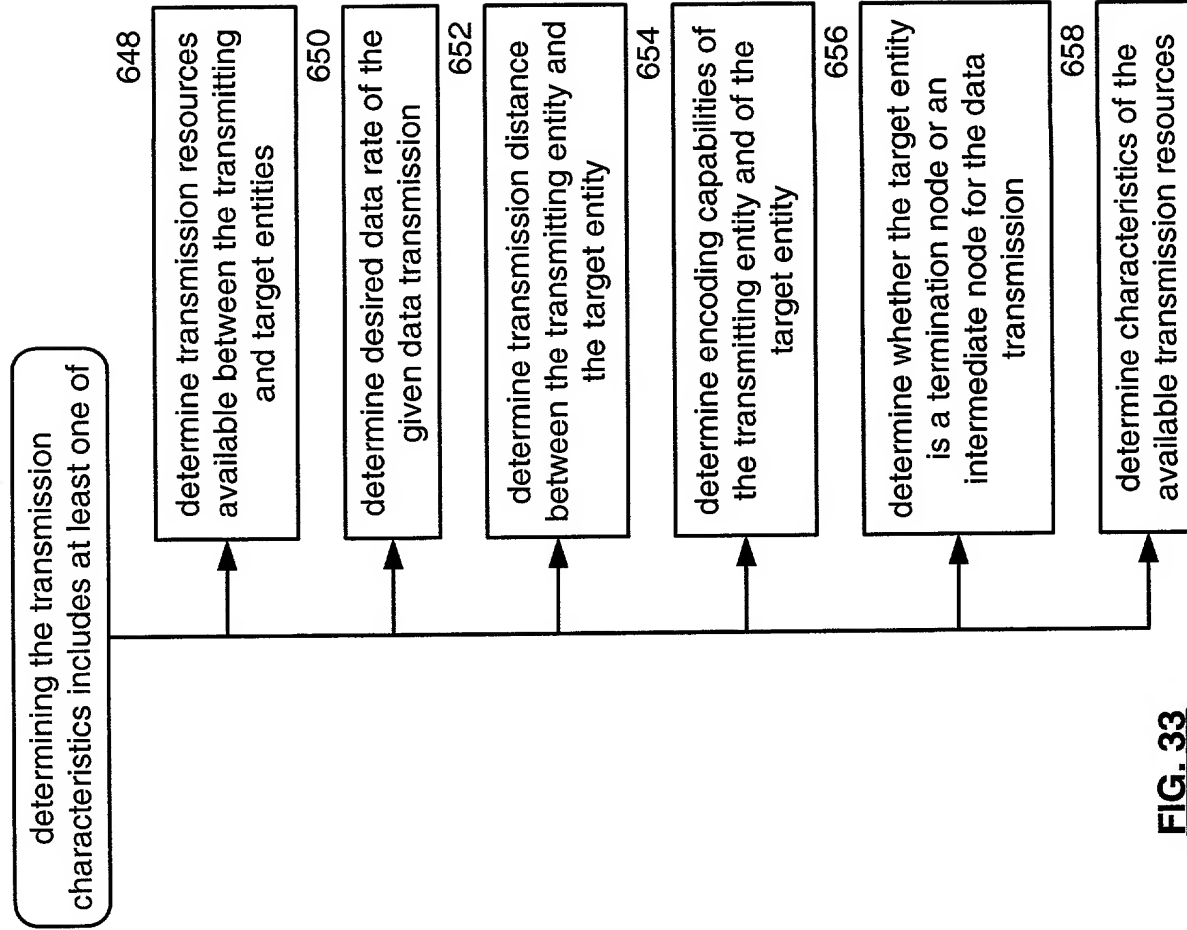


FIG. 33

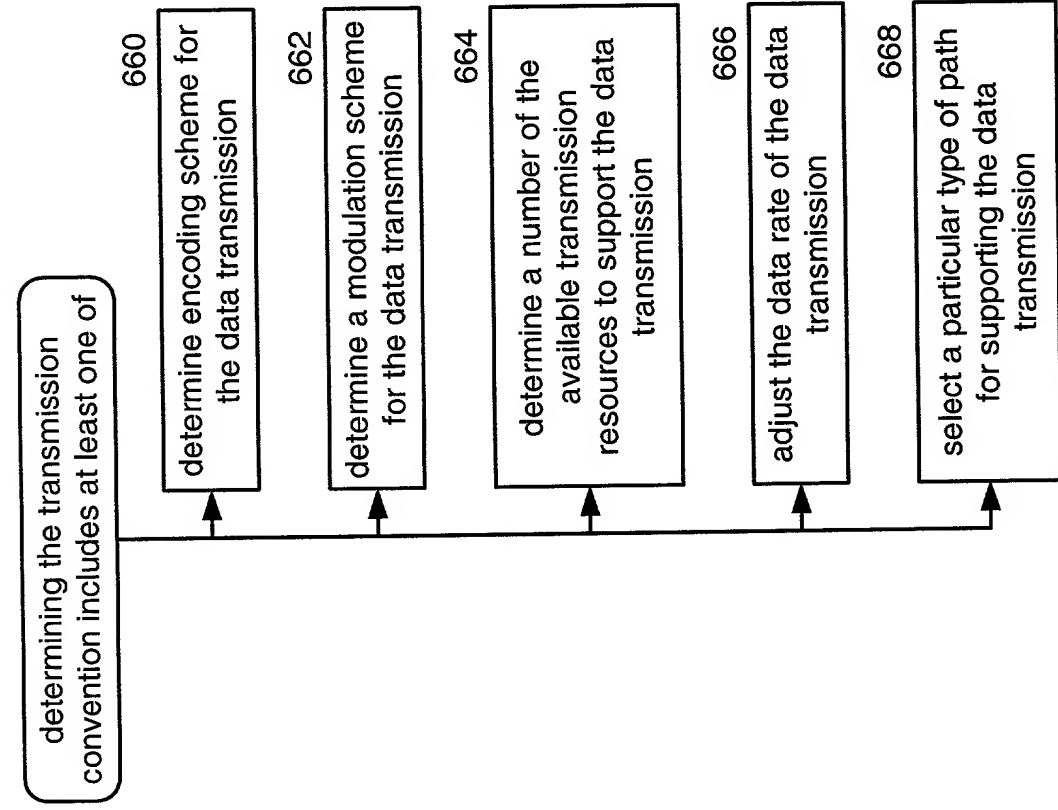


FIG. 34

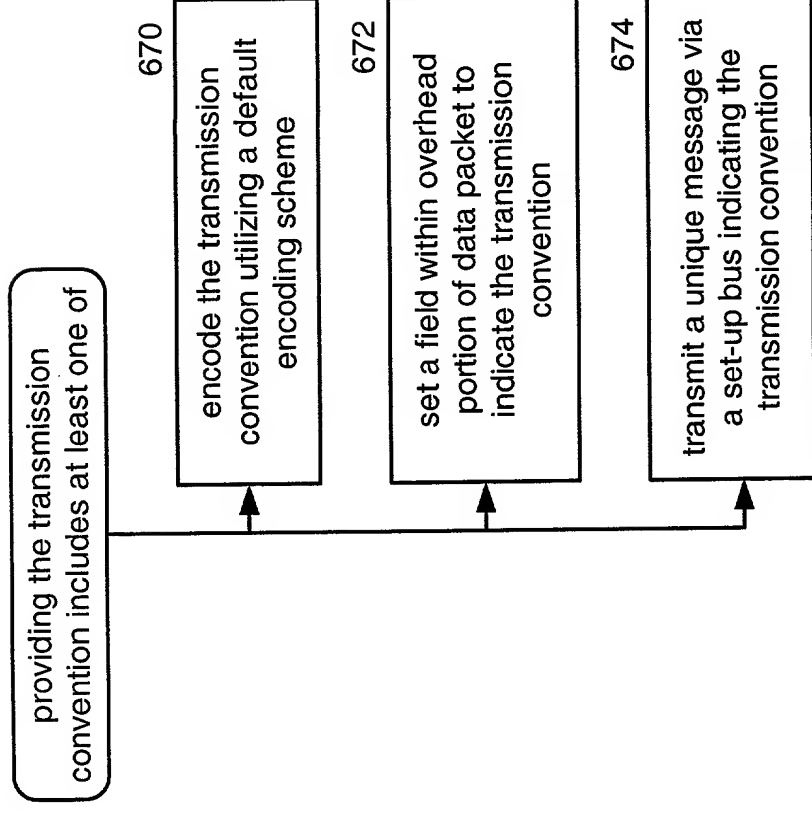


FIG. 35

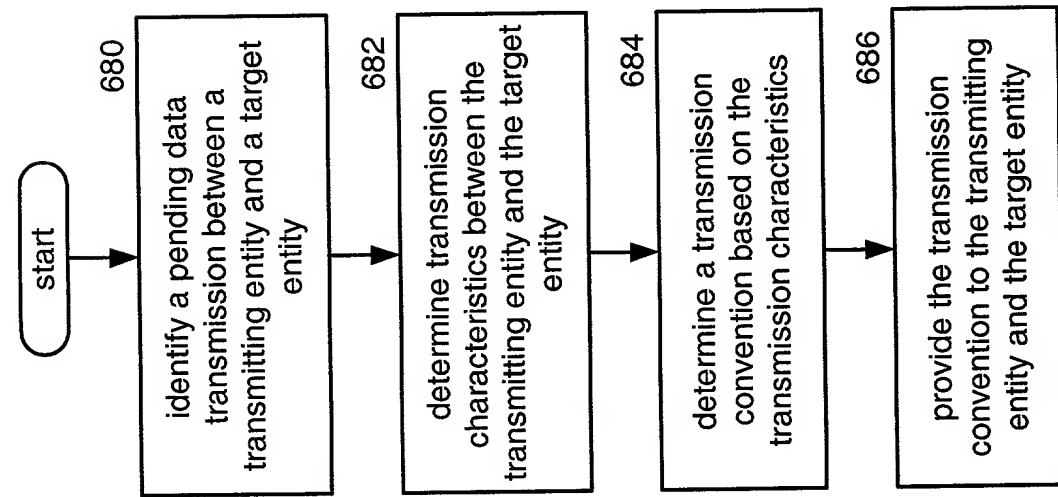


FIG. 36

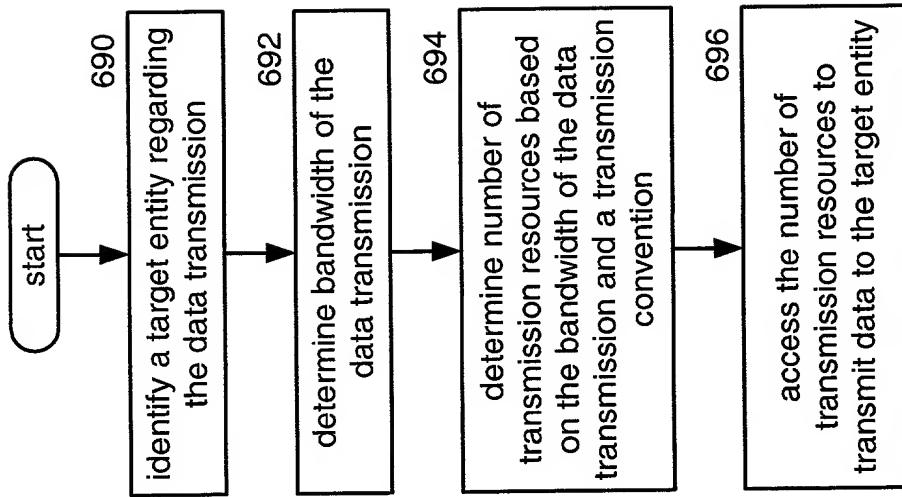


FIG. 37

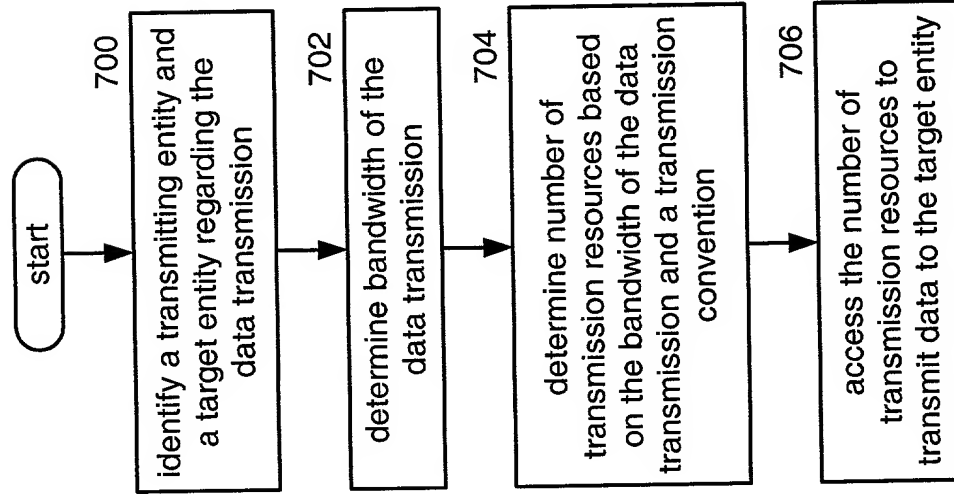


FIG. 38

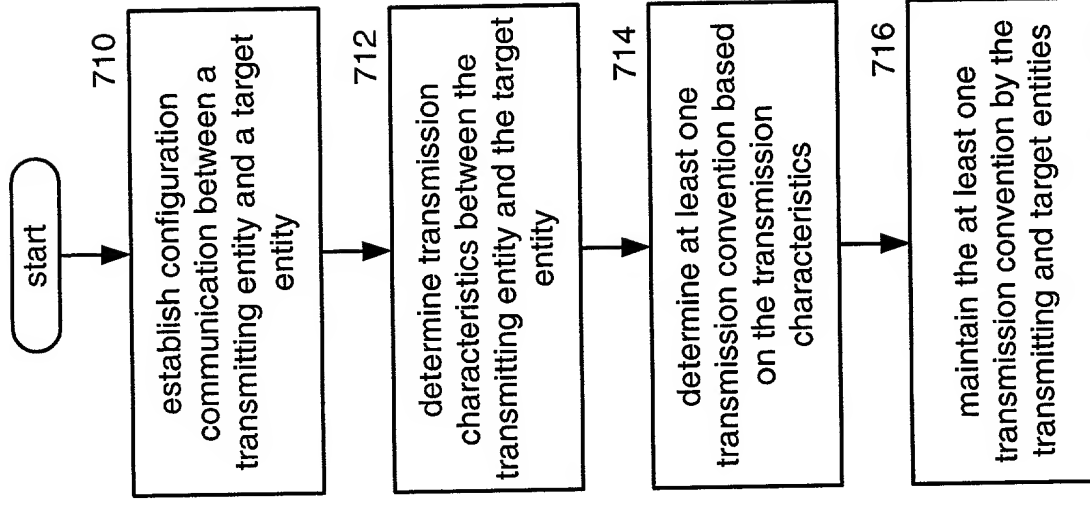


FIG. 39

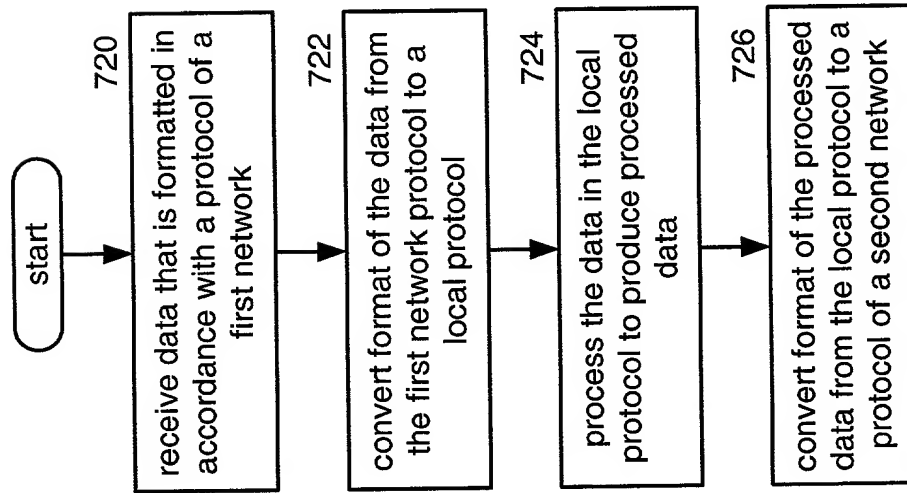


FIG. 40

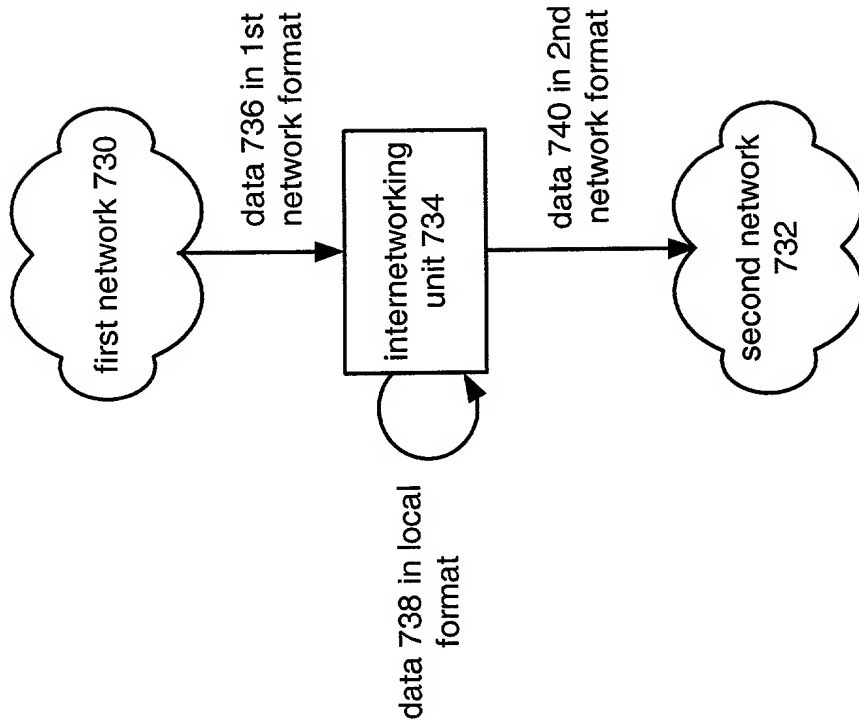


FIG. 41